



## Class Schedule by Course

### Important Notice:

1. Class Schedule list below is as of **October 07, 2008**.
2. Class dates are subject to change due to low enrollment. Please contact your local training representative if you have any questions.
3. After clicking on a list to register for a class offered by Xilinx, you will be required to login to our training catalog to checkout. If you have already created your account, please login with your username and password. If you have not created your account, please click the link "New User?" then proceed

If you have any questions, please contact the Registrar at (877) XLX-CLASS or registrar@xilinx.com

### Advanced Features and Techniques of Embedded Systems Development

Advanced Features and Techniques of Embedded Systems Development provides embedded systems developers with the necessary skills to develop complex embedded systems and enables them to improve their designs by using the tools available in the Embedded Development Kit (EDK). This course also helps developers understand and utilize advanced components of embedded systems designs for architecting a complex system.

Date	Class#	Location	Facility	Price (USD)	TC	
10/14/08 - 10/15/08	00053969	TX	Xilinx Learning Center - Dallas, TX , USA (20)	1200	12	<a href="#">Click here to Login &amp; Register</a>
11/11/08 - 11/12/08	00050304	ONTARIO	Electro-Source - Rexdale (Toronto), CANADA	1200	12	<a href="#">Click here to Login &amp; Register</a>
12/4/08 - 12/5/08	00054725	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1200	12	<a href="#">Click here to Login &amp; Register</a>

### Advanced FPGA Implementation

Advanced FPGA Implementation tackles the most sophisticated aspects of the ISE® 10.1 design suite and Xilinx hardware. Seven labs provide hands-on experience in this two-day course and cover the Xilinx Synthesis Technology (XST) tools. This course requires the Fundamentals of FPGA Design and Designing for Performance courses as prerequisites. An intermediate knowledge of Verilog or VHDL is strongly recommended as is at least six months of design experience with Xilinx tools and FPGAs. The lecture material in this course covers the ISE 10.1 tools and the Virtex®-5 and Spartan®-3E FPGAs. For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
10/29/08 - 10/30/08	00054945	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1000	10	<a href="#">Click here to Login &amp; Register</a>
11/11/08 - 11/12/08	00053684	MD		1400	14	<a href="#">Click here to Login &amp; Register</a>
12/16/08 - 12/17/08	00055883	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1000	10	<a href="#">Click here to Login &amp; Register</a>

### Advanced VHDL

Increase your VHDL proficiency by learning advanced techniques that help you write more robust and reusable code. This comprehensive course is targeted toward designers who already have some experience with VHDL. The course highlights modeling, testbenches, RTL/synthesizable design, and techniques aimed at creating parameterizable and reusable designs. For more information, see the course PDF, below.

Date	Class#	Location	Facility	Price (USD)	TC	
10/28/08 - 10/29/08	00055563	ONTARIO	Electro-Source Kanata (Ottawa) Ontario, CAN	1000	10	<a href="#">Click here to Login &amp; Register</a>
11/13/08 - 11/14/08	00053683	MD		1400	14	<a href="#">Click here to Login &amp; Register</a>

### Designing a LogiCORE PCI Express System

Attending the Designing a LogiCORE PCI Express System will provide you a working knowledge of how to implement Xilinx PCI Express® core in your applications. This course focuses on the implementation of a Xilinx PCI Express system with supporting logic and example designs. With this experience, you can improve your time to market with your PCIe core design. Various Xilinx PCI Express core products will be enumerated to aid you in selecting the proper solution. This course focuses on the Virtex®-5 FPGA PCIe Endpoint Block Plus and the Spartan®-3 PCIe integrated Endpoint PIPE block. For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
11/20/08 - 11/21/08	00053903	MD		1400	14	<a href="#">Click here to Login &amp; Register</a>
12/18/08 - 12/19/08	00055884	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1000	10	<a href="#">Click here to Login &amp; Register</a>

## Designing for Performance

Attending the Designing for Performance class will help you create more efficient designs. This course can help you fit your design into a smaller FPGA or a lower speed grade for reducing system costs. In addition, by mastering the tools and the design methodologies presented in this course, you will be able to create your design faster, shorten your development time, and lower development costs. For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
10/8/08 - 10/9/08	00053984	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1000	10	<a href="#">Click here to Login &amp; Register</a>
11/19/08 - 11/20/08	00050277	MA	Genesis Associates - Burlington, MA	1000	10	<a href="#">Click here to Login &amp; Register</a>
12/10/08 - 12/11/08	00055965	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1000	10	<a href="#">Click here to Login &amp; Register</a>
12/17/08 - 12/18/08	00055623	ONTARIO	Electro-Source Kanata (Ottawa) Ontario, CAN	1000	10	<a href="#">Click here to Login &amp; Register</a>

## Designing with Multi-Gigabit Serial I/O

Date	Class#	Location	Facility	Price (USD)	TC	
11/5/08 - 11/6/08	00055984	VA		1400	14	<a href="#">Click here to Login &amp; Register</a>
11/18/08 - 11/19/08	00053883	MD		1400	14	<a href="#">Click here to Login &amp; Register</a>

## Designing with the PlanAhead Analysis and Design Tool

Learn to increase design performance and achieve repeatable results by using the PlanAhead software tool. Topics include: a product overview, synthesis and project tips, design analysis, creating a floorplan, improving performance, experimenting with implementation options, incremental methodology, block-based IP design, and I/O pin assignment.

Note: The hands-on labs provided within this course are identical to the tutorials that are packaged with the PlanAhead tool. This course is supplemented with instructor-led presentations and demos. For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
10/13/08 - 10/14/08	00055003	NY		1400	10	<a href="#">Click here to Login &amp; Register</a>
10/15/08 - 10/16/08	00055103	GA	Hyatt Place Atlanta/Duluth/Johns Creek	1000	10	<a href="#">Click here to Login &amp; Register</a>
10/21/08 - 10/22/08	00050265	ONTARIO	Electro-Source - Rexdale (Toronto), CANADA	1000	10	<a href="#">Click here to Login &amp; Register</a>
11/11/08 - 11/12/08	00054947	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1000	10	<a href="#">Click here to Login &amp; Register</a>
11/25/08 - 11/26/08	00050266	ONTARIO	Electro-Source Kanata (Ottawa) Ontario, CAN	1000	10	<a href="#">Click here to Login &amp; Register</a>
12/2/08 - 12/3/08	00054303	MD		1400	14	<a href="#">Click here to Login &amp; Register</a>

## DSP Design Using System Generator

This course allows you to explore the System Generator tool and to gain the expertise you need to develop advanced, low-cost DSP designs. This intermediate course in implementing DSP functions focuses on learning how to use System Generator for DSP, design implementation tools, and hardware co-simulation verification. Through hands-on exercises you will implement a design from algorithm concept to hardware verification using the Xilinx FPGA capabilities. For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
11/13/08 - 11/14/08	00054948	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1200	12	<a href="#">Click here to Login &amp; Register</a>
1/13/09 - 1/14/09	00055644	ONTARIO	Electro-Source Kanata (Ottawa) Ontario, CAN	1200	12	<a href="#">Click here to Login &amp; Register</a>

## DSP Implementation Techniques for Xilinx FPGAs (v8)

This course illustrates how to take advantage of the features available in the Xilinx FPGA architecture and describes how DSP algorithms can be efficiently implemented. The techniques also demonstrate which decisions at the system level have the greatest impact on the implementation process and product costs.

Date	Class#	Location	Facility	Price (USD)	TC	
10/15/08 - 10/17/08	00053472	MD		2100	21	<a href="#">Click here to Login &amp; Register</a>

## Embedded Open-Source Linux Development

This intermediate-level, two-day course provides embedded systems developers with experience in creating an embedded open-source Linux operating system on a Xilinx development board. The course offers students hands-on experience from building the environment to booting the system using a basic, single-processor System on Chip (SoC) design with Linux 2.6 from the Xilinx kernel tree. This course introduces embedded Linux components, use of open-source components, environment configurations, network components, and debugging/profiling options for embedded Linux platforms. The primary focus is on embedded Linux development in conjunction with the Xilinx tool flow. For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
1/7/09 - 1/8/09	00055743	MA	Genesis Associates - Burlington, MA	1200	12	<a href="#">Click here to Login &amp; Register</a>

## Embedded Systems Development

Date	Class#	Location	Facility	Price (USD)	TC	
11/11/08 - 11/12/08	00054723	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1200	12	<a href="#">Click here to Login &amp; Register</a>
12/9/08 - 12/10/08	00054843	MD	Genesis Mid-Atlantic - Columbia, MD - USA (14)	1400	14	<a href="#">Click here to Login &amp; Register</a>
1/15/09 - 1/16/09	00055886	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1200	12	<a href="#">Click here to Login &amp; Register</a>

## Fundamentals of FPGA Design

Use the ISE® software tools to implement a design and gain a firm understanding of the Xilinx FPGA architecture. Learn the best design practices from the pros and understand the subtleties of the Xilinx design flow. This course covers ISE 10.1 features, such as the Architecture Wizard and the Floorplan Editor. Other topics include design planning, implementation options, and global timing constraints. For more emphasis on improving the overall design performance, take the follow-up course Designing for Performance, which builds on the basic principles covered in this course. Note that one of the prerequisites of Fundamentals of FPGA Design is the completion of the basic FPGA architecture modules. For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
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12/9/08 - 12/9/08	00055964	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	500	5	<a href="#">Click here to Login &amp; Register</a>
12/16/08 - 12/16/08	00055224	ONTARIO	Electro-Source Kanata (Ottawa) Ontario, CAN	500	5	<a href="#">Click here to Login &amp; Register</a>

## Fundamentals of FPGA Design (v9)

Use the ISE™ software tools to implement a design and gain a firm understanding of the Xilinx FPGA architecture. Learn the best design practices from the pros and understand the subtleties of the Xilinx design flow. This course covers ISE 9.1i features, such as the Architecture Wizard and the Floorplan Editor. Other topics include design planning, implementation options, and global timing constraints. For more emphasis on improving the overall design performance take the follow-up course Designing for Performance, which builds on the basic principles covered in this course.

Date	Class#	Location	Facility	Price (USD)	TC	
11/18/08 - 11/18/08	00050275	MA	Genesis Associates - Burlington, MA	500	5	<a href="#">Click here to Login &amp; Register</a>

## Introduction to Verilog

This course is a thorough introduction to the Verilog language. It emphasizes writing Register Transfer Level (RTL) and behavioral source code and targets Xilinx devices specifically and FPGA devices in general. This course also combines insightful lectures with practical lab exercises to reinforce key concepts. You will learn advanced coding techniques that will increase your overall Verilog proficiency and enhance your FPGA optimization.

Date	Class#	Location	Facility	Price (USD)	TC	
11/3/08 - 11/5/08	00054946	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1500	15	<a href="#">Click here to Login &amp; Register</a>

## Introduction to VHDL

This comprehensive course is a thorough introduction to the VHDL language. The emphasis is on writing Register Transfer Level (RTL) and behavioral source code. This class addresses targeting Xilinx devices specifically and FPGA devices in general. The information gained can be applied to any digital design by using a top-down synthesis design approach. You will also learn advanced coding techniques that will increase your overall VHDL proficiency and prepare you for the Advanced VHDL course.

Date	Class#	Location	Facility	Price (USD)	TC	
10/22/08 - 10/24/08	00053471	MD		2100	21	<a href="#">Click here to Login &amp; Register</a>

## Minimizing Your Design Time with the ChipScope Pro Debug and Verification Tools

Date	Class#	Location	Facility	Price (USD)	TC	
12/3/08 - 12/3/08	00054724	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	500	5	<a href="#">Click here to Login &amp; Register</a>
12/4/08 - 12/4/08	00054305	MD		700	7	<a href="#">Click here to Login &amp; Register</a>
2/3/09 - 2/3/09	00054743	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	500	5	<a href="#">Click here to Login &amp; Register</a>

## VHDL: Advanced VHDL (v8)

Increase your VHDL proficiency by learning advanced techniques that help you write more robust and reusable code. This comprehensive course is targeted toward designers who already have some experience with VHDL. The course highlights modeling, testbenches, RTL/synthesizable design, and techniques aimed at creating parameterizable and reusable designs. For more information, see the course PDF, below.

Date	Class#	Location	Facility	Price (USD)	TC	
10/23/08 - 10/24/08	00054944	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1000	10	<a href="#">Click here to Login &amp; Register</a>

**VHDL: Introduction to VHDL (v8)**

This course introduces the VHDL language with emphasis on targeting Xilinx devices specifically and FPGA devices in general. This course also combines insightful lecture with practical lab exercises to reinforce key concepts. You can then apply this information to any digital design using a Top-Down synthesis design approach.

<b>Date</b>	<b>Class#</b>	<b>Location</b>	<b>Facility</b>	<b>Price (USD)</b>	<b>TC</b>	
12/3/08 - 12/5/08	00055564	ONTARIO	Electro-Source - Rexdale (Toronto), CANADA	1500	15	<a href="#">Click here to Login &amp; Register</a>