



MEDIA ALERT

Accelize Debuts New Distribution Platform for FPGA-Accelerated Software at Xilinx Developer Forum (XDF) 2019

Aix-en-Provence, France & San Jose, California - September 24, 2019

WHO: Accelize, a Xilinx partner that provides a new distribution platform for scaling, managing and monetizing field-programmable gate array (FPGA) accelerated software.

WHAT: Exhibiting at the Xilinx Developer Forum (XDF), Accelize will showcase its new distribution and monetization platform for FPGA accelerated software and make it available free to Xilinx developers. The Accelize platform provides developers of FPGA applications and IP cores with a seamless environment for monetizing, scaling and protecting intellectual property as well as automatic billing, invoicing and payments. For on-premise and cloud application deployment, the platform works with frameworks based on Kubernetes, Kubuless, Docker, Ansible and Terraform. In partnership with Xilinx, Accelize is offering free use of the environment for an introductory period to Xilinx developers who sign up [here](#).

WHEN: Tuesday and Wednesday, October 1 and 2, 2019

WHERE: Fairmont San Jose, San Jose, California

About XDF

The Xilinx Developer Forum connects software developers and system designers to the deep expertise of Xilinx engineers, partners, and industry leaders to enable insights and inspiration for application and system design.

About Accelize

[Accelize](#) is creating a global distribution platform for FPGA-accelerated software. Its peer-to-peer white-label marketplace makes domain-specific expertise and innovation from FPGA app and IP core developers easily available to more users.

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Caption:

At XDF Accelize will demonstrate a video encoded live with a new UHD HEVC FPGA app developed by V-Nova and NGCodec/Xilinx running on a Xilinx Alveo card. In this demo, the Accelize platform is showed branded for Xilinx/NGCodec.