

		Defense-Grade FPGAs					
		Virtex®-6Q FPGAs					
		Part Number	XQ6VLX130T	XQ6VLX240T	XQ6VLX550T	XQ6VXSX315T	XQ6VXSX475T
Logic Resources	Logic Cells		128,000	241,000	550,000	315,000	476,000
	Maximum Distributed RAM (Kb)		1,740	3,650	6,200	5,090	7,640
Memory Resources	Block RAM/FIFO w/ECC (36 Kb each)		264	416	632	704	1064
	Total Block RAM (Kb)		9,504	14,976	22,752	25,344	38,304
Clock Resources	Mixed Mode Clock Manager (MMCM)		10	12	18	12	18
I/O Resources	Maximum Single-Ended Pins		600	720	840	720	840
Embedded Hard IP Resources	DSP48E1 Slices		480	768	864	1,344	2,016
	Interface Blocks for PCI Express®		2	2	2	2	2
	10/100/100 Ethernet MAC Blocks		4	4	4	4	4
	GTX Low-Power Transceivers		20	24	36	24	36
Miscellaneous	Speed Grades: Military (M: -55°C - 125°C)		-1	-1	—	-1	—
	Speed Grades: Industrial (I: -40°C - 100°C)		-1, -2, -1L	-1, -2, -1L	-1, -1L	-1, -2, -1L	-1, -1L
	Package	Area	Maximum User I/O: SelectIO™ Interface Pins (GTX Serial Transceivers)				
	RF784 <sup>(1)</sup>	29 X 29 mm	400 (12)	400 (12)			
	RF1156	35 x 35 mm	600 (20)	600 (20)		600 (20)	600 (20)
	FFG1156 <sup>(2)</sup>	35 x 35 mm	600 (20)	600 (20)		600 (20)	600 (20)
	RF1759	42.5 x 42.5 mm		720 (24)	840 (36)	720 (24)	840 (36)

XMP076 (v2.3)

Notes: 1. RF is ruggedized, leaded flip chip package.  
2. No M-temp for FFG.