

Declaration of Conformity

In accordance with EN ISO/IEC 17050-1:2010

Manufacturers Name: XILINX, Inc.
Manufacturers Address: 2100 Logic Drive
San Jose, CA 95124
U.S.A.

Application of Council Directives:

Low Voltage 2006/95/EC
EMC 2004/108/EC

Standards:

EMC EN55022:2010
EN55024:2010
Safety IEC 60950-1:2005
EN 60950-1:2006

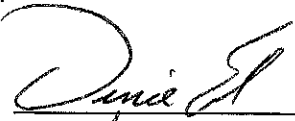
Product Name: Kintex-7 FPGA Connectivity Kit

Product Model Number: DK-K7-CONN-G
DK-K7-CONN-G-J
DK-K7-CONN-CES-G
DK-K7-CONN-CES-G-J

We, the undersigned, hereby declare that the equipment specified above conforms to the above Directives and Standards.

Location: Lowmont, Co, USA

Date: 9/29/2013

Signature: 

Full Name (print): VANCE ECK

Title: ENGINEERING MANAGER

© Copyright 2013 Xilinx, Inc. Xilinx, the Xilinx logo, Artix, ISE, Kintex, Spartan, Virtex, Vivado, Zynq, and other designated brands included herein are trademarks of Xilinx in the United States and other countries. All other trademarks are the property of their respective owners.

DISCLAIMER

The information disclosed to you hereunder (the "Materials") is provided solely for the selection and use of Xilinx products. To the maximum extent permitted by applicable law: (1) Materials are made available "AS IS" and with all faults, Xilinx hereby DISCLAIMS ALL WARRANTIES AND CONDITIONS, EXPRESS, IMPLIED, OR STATUTORY, INCLUDING BUT NOT LIMITED TO WARRANTIES OF MERCHANTABILITY, NON-INFRINGEMENT, OR FITNESS FOR ANY PARTICULAR PURPOSE; and (2) Xilinx shall not be liable (whether in contract or tort, including negligence, or under any other theory of liability) for any loss or damage of any kind or nature related to, arising under, or in connection with, the Materials (including your use of the Materials), including for any direct, indirect, special, incidental, or consequential loss or damage (including loss of data, profits, goodwill, or any type of loss or damage suffered as a result of any action brought by a third party) even if such damage or loss was reasonably foreseeable or Xilinx had been advised of the possibility of the same. Xilinx assumes no obligation to correct any errors contained in the Materials or to notify you of updates to the Materials or to product specifications. You may not reproduce, modify, distribute, or publicly display the Materials without prior written consent. Certain products are subject to the terms and conditions of Xilinx's limited warranty, please refer to Xilinx's Terms of Sale which can be viewed at <http://www.xilinx.com/legal.htm#tos>.

Xilinx products are not designed or intended to be fail-safe or for use in any application requiring fail-safe performance; you assume sole risk and liability for use of Xilinx products in such critical applications, please refer to Xilinx's Terms of Sale which can be viewed at <http://www.xilinx.com/legal.htm#tos>.