



REV	DESCRIPTION	APPD BY	DATE
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DESCRIPTION
ARTWORK, ML405 EVAL PLATFORM PCB

DRG. NO.
0531487

SIZES	QTY	SYN	PLTD
12	29	X	PLTD
81	2	Q	PLTD
42	90	Q	PLTD
38	5	X	PLTD
40	54	M	PLTD
35	48	A	PLTD
27	4	B	PLTD
27	15	C	PLTD
47	20	D	PLTD
13	32	E	PLTD
16	30	G	PLTD
15	9	H	PLTD
125	11	I	PLTD
27	324	J	PLTD
47	4	K	PLTD
85	30	L	PLTD
43	75	M	PLTD
120	2	N	PLTD
32	18	O	PLTD
107	2	P	PLTD
10	2953	Q	PLTD
51	23	R	PLTD
19	8	S	WPLTD
29	4	T	PLTD
105	2	U	PLTD
19	2	V	PLTD
42	2	W	WPLTD
15.75	124	X	PLTD
43	24	Y	PLTD
10	2	Z	PLTD
27 x 86.41	4	A	PLTD
27 x 80	6	J	PLTD
27 x 86.41	2	B	WPLTD

- NOTES: UNLESS OTHERWISE SPECIFIED.**
- APPLICABLE STANDARDS/SPECIFICATIONS: FABRICATE BOARD PER IPC-A-6000 STANDARD.
 - PRIMARY SIDE SHOWN.
 - MATERIAL: 203 TMM, FR-4, NATURAL GREEN, UL94V-0 COMPLIANT. START 1/2 OZ COPPER BOBINA SURFACES. ALL PLANES ARE 1 OZ COPPER.
 - ALL HOLES TO BE PLATED THRU. PLATING IN HOLES TO BE .001 MM THICKNESS. HOLE SIZES ARE AFTER PLATING. HOLE TOLERANCE +/- .003 EXCEPT VIAS. VIA TOLERANCE +/- .005/- .004 OF HOLE DIAMETER.
 - PLATING: GOLD IMMERSION.
 - USE ARTIMASTER 9 ML405.
 - FINISHED THICKNESS TO BE +/- .005 OR .5%, WHICHEVER IS GREATER.
 - SLKSCREEN TOP & BOTTOM SIDE WITH WHITE NON-CONDUCTIVE EPOXY INK.
 - FABRICATE PER DIMENSIONS SHOWN. BOARD DIMENSION TOLERANCE'S: X, Y: +/- .01. Z: +/- .003.
 - BREAK ALL SHARP EDGES.
 - CONTROL IMPEDANCE SEE CHART BELOW. STACKUP IN EXCEL FORMAT IS PROVIDED.
 - REMOVE ALL UNUSED PADS ON INTERNAL TRACE LAYERS, LAYERS 2, 4, 6, 7, 9 & 11.
 - SIGNAL ENDED IMPEDANCE CONTROL NETS ON TOP LAYER. ADJUST IT EXACTLY 75-OHM.
 - NETS ARE 20MILS BONDING BOND. ADJUST IT EXACTLY 75-OHM.
 - FIVE LOCATIONS (TOTAL TEN COMPONENTS) COMPONENTS ARE PLACED ON COMP & SOLDER SIDE SO THAT THESE PTH PINS ARE EXACTLY MATCH IN X & Y LOCATION. TOTAL OF 1245=60 DRILLS

LAYER STACK-UP	THICKNESS	PREP	ALL NUMBER LAYER X-Y
1 Top Side	0.035mm	0.025mm	0.025mm
2 PRE PLANE 1	0.035mm	0.025mm	0.025mm
3 PRE PLANE 1	0.035mm	0.025mm	0.025mm
4 PRE PLANE 1	0.035mm	0.025mm	0.025mm
5 PRE PLANE 1	0.035mm	0.025mm	0.025mm
6 PRE PLANE 2	0.035mm	0.025mm	0.025mm
7 PRE PLANE 2	0.035mm	0.025mm	0.025mm
8 PRE PLANE 2	0.035mm	0.025mm	0.025mm
9 PRE PLANE 2	0.035mm	0.025mm	0.025mm
10 PRE PLANE 2	0.035mm	0.025mm	0.025mm
11 PRE PLANE 2	0.035mm	0.025mm	0.025mm
12 BOTTOM SIDE	0.035mm	0.025mm	0.025mm

FABRICATION / DRILL DRAWING			
APPROVALS	DATE	UNLESS OTHERWISE SPECIFIED: ALL DIMS IN INCHES & INCLUDE DIMEN. APPLIED/PLATED FINISH. DO NOT SCALE DRAWING. REMOVE BURRS & SHARP EDGES.	COMPANY: XILINX 2100 LOGIC DRIVE SAN JOSE, CA 95124
CHECKED KHANG DAO	26-08-05	TOL'S: X/Z: Y/Z: ANGLES: WAVE'S	TITLE: ML405 PCB
APPR		FINISH	SIZE: C CORP. NO. PCB P/N: 128XXXX
			SCALE: N/A
			SHEET 1 OF 1

XILINX PART NUMBER PCB P/N: 128XXXX SHT REV: A