

## Overview

The purpose of this notice is to announce the addition of STATS ChipPAC in Singapore (SCS) as a qualified assembly partner for Plastic Quad Flat Pack (PQFP), Thin Quad Flat Pack (TQFP/VQFP), and Ball Grid Array in wire bond (BGA) packages. There is no change to the form, fit, or function of these devices and packages. The package material set (substrate, leadframe, mold compound, and die attach), as well as the manufacturing processes, are compatible with PQFP, TQFP, and BGA packages that Xilinx currently supplies from other qualified assembly partners.

## Description

The reason for this change is to expand additional assembly capacity for PQFP, TQFP, and BGA packages.

## Products Affected

This change affects Plastic Quad Flat Pack (PQFP), Thin Quad Flat Pack (TQFP), and Ball Grid Array (BGA) packages for Spartan™, CoolRunner™, and XC9500™ product families.

A complete list of affected devices is available in the [Appendix](#).

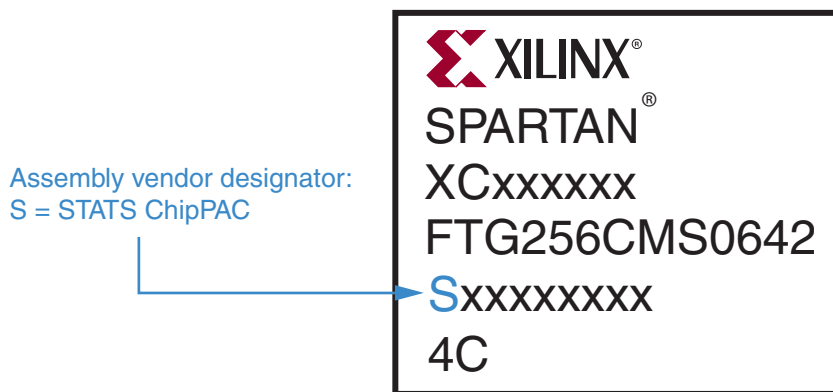
## Key Dates and Ordering Information

Due to increasing demand, devices with a date code of 0645 (or later) can be assembled at either STATS ChipPAC or other qualified assembly partners. This will be shipped to customers against orders for standard product starting January 2007.

This is a change from the starting date code of 0701 stated in the previous version (v1.0) of this PCN.

## Traceability

Affected devices are identified by the assembly vendor designator as shown in [Figure 1](#).



XCN06016\_01\_062306

Figure 1: Package Topmark

## Qualification Data

Xilinx performed an on-site audit of this facility in October 2005 and verified STATS ChipPAC conformance to Xilinx and ISO9001:2000, ISO/TS16949 and ISO14001 Quality Systems Requirements. The lead products used to qualify STATS ChipPAC packages are listed in Table 1 below. Qualification data of these packages is available now and can be found in the following document: [RPT058 - Qualification Report for XCN06016 - New Assembly Partner \(SCS\)](#).

Table 1: Lead Qualification Products

	Package	Device
Ball Grid Array (BGA)	FGG676	XC2S600E
	FTG256	XC2S400E
	CPG132	XC3S500E
Plastic Quad Flat Pack (PQFP)	PQG208	XC3S500E
Thin Quad Flat Pack (TQFP/VQFP)	TQG144	XC3S250E
	VQG100	XC3S250E

Successful qualification of these selected products qualifies all other listed products in these packages. In addition, all Pb-free versions are also qualified.

## Recommendation

No response is required to this PCN. For additional information or questions, please contact [Xilinx Technical Support](#).

**Important Notice:** Xilinx Customer Notifications (PCN, PDN, and Quality Alerts) can be delivered via e-mail alerts sent by the MySupport website (<http://www.xilinx.com/support>). Register today and personalize your “MyAlerts” to include Customer Notifications. This change provides many benefits, including the ability to receive alerts for new and updated information about specific products, as well as alerts for other publications such as data sheets, errata, application notes, and so forth. For instructions on how to sign up, refer to [Xilinx Answer Record 18683](#).

## Appendix

XCS05VQ100	XC2S150FGG256	XC2S200EFGG456	XC3S1500FG456
XCS05VQG100	XC2S200FG456	XC2S200EFT256	XC3S1500FGG456
XCS05PC84	XC2S200FGG456	XC2S200EFTG256	XC3S1500FG676
XCS10TQ144	XC2S200PQ208	XC2S200EPQ208	XC3S1500FGG676
XCS10VQ100	XC2S200PQG208	XC2S200EPQG208	XC3S1500FG320
XCS10VQG100	XC2S200FG256	XC2S300EFG456	XC3S1500FGG320
XCS10PC84	XC2S200FGG256	XC2S300EFGG456	XC3S200FT256
XCS20PQ208	XC2S30CS144	XC2S300EFT256	XC3S200FTG256
XCS20PQG208	XC2S30CSG144	XC2S300EFTG256	XC3S200PQ208
XCS20TQ144	XC2S30PQ208	XC2S300EPQ208	XC3S200PQG208
XCS20TQG144	XC2S30TQ144	XC2S300EPQG208	XC3S200TQ144
XCS20VQ100	XC2S30TQG144	XC2S400EFG456	XC3S200TQG144
XCS20VQG100	XC2S30VQ100	XC2S400EFGG456	XC3S200VQ100
XCS30PQ208	XC2S30VQG100	XC2S400EFT256	XC3S200VQG100
XCS30PQG208	XC2S50PQ208	XC2S400EFTG256	XC3S2000FG456
XCS30TQ144	XC2S50PQG208	XC2S400EFG676	XC3S2000FGG456
XCS30VQ100	XC2S50TQ144	XC2S400EFGG676	XC3S2000FG676
XCS30PQ240	XC2S50TQG144	XC2S50EFT256	XC3S2000FGG676
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XCS40PQ240	XC2S50FGG256	XC2S50EPQ208	XC3S2000FGG900
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XC2S100TQ144	XC2S100EFT256	XC2S50ETQG144	XC3S400FT256
XC2S100TQG144	XC2S100EFTG256	XC2S600EFG456	XC3S400FTG256
XC2S100FG256	XC2S100EPQ208	XC2S600EFGG456	XC3S400PQ208
XC2S100FGG256	XC2S100EPQG208	XC2S600EFG676	XC3S400PQG208
XC2S15TQ144	XC2S100ETQ144	XC2S600EFGG676	XC3S400TQ144
XC2S15TQG144	XC2S100ETQG144	XC3S1000FG456	XC3S400TQG144
XC2S15VQ100	XC2S150EFG456	XC3S1000FGG456	XC3S400FG320
XC2S15VQG100	XC2S150EFGG456	XC3S1000FT256	XC3S400FGG320
XC2S150FG456	XC2S150EFT256	XC3S1000FTG256	XC3S4000FG676
XC2S150FGG456	XC2S150EFTG256	XC3S1000FG676	XC3S4000FGG676
XC2S150PQ208	XC2S150EPQ208	XC3S1000FGG676	XC3S4000FG900
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XC3S50PQ208	XC3S250EVQG100	XCR3256XLFT256	XC2C32APCG44
XC3S50PQG208	XC3S250ECP132	XCR3256XLFTG256	XC2C32AVQ44
XC3S50TQ144	XC3S250ECPG132	XCR3256XLPQ208	XC2C32AVQG44
XC3S50TQG144	XC3S500EFT256	XCR3256XLPQG208	XC2C384FT256
XC3S50VQ100	XC3S500EFTG256	XCR3256XLTQ144	XC2C384FTG256
XC3S50VQG100	XC3S500EPQ208	XCR3256XLTQG144	XC2C384PQ208
XC3S50CP132	XC3S500EPQG208	XCR3384XLFT256	XC2C384PQG208
XC3S50CPG132	XC3S500ECP132	XCR3384XLPQ208	XC2C384TQ144
XC3S5000FG900	XC3S500ECPG132	XCR3384XLPQG208	XC2C384TQG144
XC3S5000FGG900	XC3S500EFG320	XCR3384XLTQ144	XC2C384FG324
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XC3S1200EFGG320	XCR3064XLCS48	XC2C128VQG100	XC2C64APC44
XC3S1200EFG400	XCR3064XLCSG48	XC2C128CP132	XC2C64APCG44
XC3S1200EFGG400	XCR3064XLPC44	XC2C128CPG132	XC2C64AVQ100
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XC3S250EPQ208	XCR3128XLTQG144	XC2C256CP132	XC95108PQG160
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XC3S250ETQ144	XCR3128XLVQG100	XC2C32ACP56	XC95108TQG100

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XC9572VQ64	XC9536XLCSG48	XC95288XVCS280	
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XC9572PQ100	XC9536XLVQ44	XC95288XVPQG208	

## Revision History

The following table shows the revision history for this document.

Date	Version	Revision
9/25/06	1.0	Initial release.
12/25/06	1.1	Revised the <a href="#">Key Dates and Ordering Information</a> section.