



# Flip Chip Substrates BT-to-ABF Conversion for Select Virtex-II Pro FPGA and Virtex-4 FPGA Devices

XCN09013 (v1.0) October 5, 2009

Product Change Notice

## Overview

The purpose of this notification is to announce conversion of substrate material changed from BT to ABF build-up for select Virtex<sup>®</sup>-II Pro FPGA and Virtex<sup>®</sup>-4 FPGA device/package. There is no change to the form, fit, function or reliability with this change.

## Description

For the below affected products, substrate materials will be converted from BT to ABF build-up. The reason for the change is the phase out of UV laser equipment support at substrate supplier. To ensure continuity of supply for flip chip products, BT will be replaced by ABF since UV laser is not required to form vias with ABF material. The ABF materials set is already qualified and in production for other Virtex-II Pro FPGA, Virtex-4 FPGA and Virtex<sup>®</sup>-5 FPGA products.

## Products Affected

This change affects all speed, package, and temperature variations of the commercial (C) and industrial (I) grade devices. Automotive devices are not affected by this change. Affected part numbers are included in the following table(s):

Table 1: Product Changes

Xilinx Product	Package	Change Description
XC4VLX15	FF(G)668	Build-up materials set change from BT to ABF
XC4VLX40	FF(G)668	Build-up materials set change from BT to ABF
XC4VSX25	FF(G)668	Build-up materials set change from BT to ABF
XC4VLX15	SF(G)363	Build-up materials set change from BT to ABF
XC4VLX200	FF(G)1513	Build-up materials set change from BT to ABF
XC4VFX140	FF(G)1517	Build-up materials set change from BT to ABF
XC2VP70	FF(G)1517	Build-up materials set change from BT to ABF

Note: XC2VP70-FF(G)1704 and XC2VP100-FF(G)1704 are under review and may be included in a future PCN.

## Key Dates and Ordering Information

Xilinx will begin to cross-ship production using BT and ABF substrate materials on or after 90 days of PCN date.

## Qualification Data

Qualification data & device reliability report for Virtex-II Pro FPGA and Virtex-4 FPGA devices is available in [UG116](#). The extended qualification data related to this PCN is available in [RPT141](#) located in 'Additional Document' section below.

## Response

No response is required. For additional information or questions, please contact [Xilinx Technical Support](#).

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## Additional Documentation

Qualification Report (RPT141)  
<https://secure.xilinx.com/webreg/clickthrough.do?cid=140202>

Device Reliability Report (UG116):  
[http://www.xilinx.com/support/documentation/user\\_guides/ug116.pdf](http://www.xilinx.com/support/documentation/user_guides/ug116.pdf)

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## Revision History

The following table shows the revision history for this document.

Date	Version	Revision
10/05/09	1.0	Initial release.

## Disclaimer

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