

Overview

The purpose of this notification is to add Amkor Technology Philippines “ATP”, as an additional test site for Xilinx Automotive (XA) Spartan[®]-3/-3E/-3A/-3A DSP devices. There is no change to the form, fit, or function.

Description

Xilinx supply chain model has always qualified more than one test site for supply continuity. Xilinx has qualified SPIL and Amkor to correlate to Xilinx test facilities for final electrical testing. This notification is to inform you that Xilinx plans to ship production material for all XA Spartan-3/-3E/-3A/-3A DSP devices from SPIL-Taiwan, Amkor Philippines (ATP) and Xilinx Singapore (XAP). SPIL and ATP will be the primary test sites. Xilinx “XAP” will be used, as required. All 3 locations use the same ATE platforms for each specific product family. They all have been qualified for high-volume commercial devices (XC), as well as for the automotive (XA) product families.

There is no change to the form, fit, or function. There is no change to the tester platform or program associated with this notification. All three sites are using the same tester platform, test program and process.

Products Affected

This change affects all XA Spartan-3/-3E/-3A/-3A DSP (I Grade and Q Grade) devices including all versions under specification control documentation (SCD) reference.

Table 1: Xilinx Automotive Spartan-3 FPGA Affected Devices

Density	Xilinx Part Number	Density	Xilinx Part Number	
XA3S50	XA3S50-4VQG100I	XA3S400	XA3S400-4FTG256I	
	XA3S50-4VQG100Q		XA3S400-4FTG256Q	
	XA3S50-4PQG208I		XA3S400-4PQG208I	
	XA3S50-4PQG208Q		XA3S400-4PQG208Q	
XA3S200	XA3S200-4VQG100I		XA3S400-4FGG456I	
	XA3S200-4VQG100Q		XA3S400-4FGG456Q	
	XA3S200-4TQG144I		XA3S1000	XA3S1000-4FTG256I
	XA3S200-4TQG144Q			XA3S1000-4FTG256Q
	XA3S200E-4PQG208I	XA3S1000-4FGG456I		
	XA3S200E-4PQG208Q	XA3S1000-4FGG456Q	XA3S1500	XA3S1500-4FGG456I
	XA3S200E-4FTG256I	XA3S1500-4FGG676I		
	XA3S200E-4FTG256Q			

Table 2: Xilinx Automotive Spartan-3E FPGA Affected Devices

Density	Xilinx Part Number	Density	Xilinx Part Number
XA3S100E	XA3S100E-4VQG100I	XA3S500E	XA3S500E-4CPG132I
	XA3S100E-4VQG100Q		XA3S500E-4CPG132Q
	XA3S100E-4CPG132I		XA3S500E-4PQG208I
	XA3S100E-4CPG132Q		XA3S500E-4PQG208Q
	XA3S100E-4TQG144I		XA3S500E-4FTG256I
	XA3S100E-4TQG144Q		XA3S500E-4FTG256Q
XA3S250E	XA3S250E-4VQG100I	XA3S1200E	XA3S1200E-4FTG256I
	XA3S250E-4VQG100Q		XA3S1200E-4FTG256Q
	XA3S250E-4CPG132I		XA3S1200E-4FGG400I
	XA3S250E-4CPG132Q		XA3S1200E-4FGG400Q
	XA3S250E-4TQG144I	XA3S1600E	XA3S1600E-4FGG400I
	XA3S250E-4TQG144Q		XA3S1600E-4FGG400Q
	XA3S250E-4PQG208I		XA3S1600E-4FGG484I
	XA3S250E-4PQG208Q		XA3S1600E-4FGG484Q
	XA3S250E-4FTG256I		
	XA3S250E-4FTG256Q		

Table 3: Xilinx Automotive Spartan-3A FPGA Affected Devices

Density	Xilinx Part Number	Density	Xilinx Part Number
XA3S200A	XA3S200A-4FTG256I	XA3S700A	XA3S700A-4FGG400I
	XA3S200A-4FTG256Q		XA3S700A-4FGG400Q
XA3S400A	XA3S400A-4FTG256I		XA3S700A-4FGG484I
	XA3S400A-4FTG256Q		XA3S700A-4FGG484Q
	XA3S400A-4FGG400I	XA3S1400A	XA3S1400A-4FGG484I
	XA3S400A-4FGG400Q		XA3S1400A-4FGG484Q

Table 4: Xilinx Automotive Spartan-3A DSP FPGA Affected Devices

Density	Xilinx Part Number	Density	Xilinx Part Number
XA3SD1800A	XA3SD1800A-4CSG484I	XA3SD3400A	XA3SD3400A-4CSG484I
	XA3SD1800A-4CSG484Q		XA3SD3400A-4FGG676I
	XA3SD1800A-4FGG676I		
	XA3SD1800A-4FGG676Q		

Key Dates and Ordering Information

1. Within 30 days after release of this change notification, the customer must acknowledge to Xilinx receipt of the document.
2. Additional requirements, if any, need to be identified to Xilinx within 60 days of this notification. Requirements requested after this date may not be supported.
3. Within one year after the release of this change notification, a customer approval is required.
4. Existing PPAP documentation will be revised by Xilinx and submitted to customers within 90 days of this notification.
5. PSW (Parts Submission Warrant) must be signed and returned to Xilinx upon approval of this change notice: devices will be standard orderable parts.

Traceability

There is no change to the form, fit, or function.

Qualification Data

Test Correlation data is available and provided with this notification ([RPT111](#)).

Recommendations

Please refer to the key dates and ordering section listed with this notification.

Response

A customer response is required: Please refer to the key dates and ordering section listed with this notification.

Important Notice: Xilinx Customer Notifications (XCNs, XDNs, and Quality Alerts) can be delivered via e-mail alerts sent by the MySupport website (<http://www.xilinx.com/support>). Register today and personalize your “MyAlerts” area to include Customer Notifications. Xilinx MySupport provides many benefits, including the ability to receive alerts for new and updated information about specific products, as well as alerts for other publications such as data sheets, errata, application notes, etc. For information on how to sign up, refer to [Xilinx Answer Record 18683](#).

Additional Documentation

Qualification Report (RPT111):

<https://secure.xilinx.com/webreg/clickthrough.do?cid=143382>.

Revision History

The following table shows the revision history for this document:

Date	Version	Description of Revisions
03/01/10	1.0	Initial release.

Notice of Disclaimer

THE XILINX HARDWARE FPGA AND CPLD DEVICES REFERRED TO HEREIN (“PRODUCTS”) ARE SUBJECT TO THE TERMS AND CONDITIONS OF THE XILINX LIMITED WARRANTY WHICH CAN BE VIEWED AT <http://www.xilinx.com/warranty.htm>. THIS LIMITED WARRANTY DOES NOT EXTEND TO ANY USE OF PRODUCTS IN AN APPLICATION OR ENVIRONMENT THAT IS NOT WITHIN THE SPECIFICATIONS STATED ON THE XILINX DATA SHEET. ALL SPECIFICATIONS ARE SUBJECT TO CHANGE WITHOUT NOTICE. PRODUCTS ARE NOT DESIGNED OR INTENDED TO BE FAIL-SAFE, OR FOR USE IN ANY APPLICATION REQUIRING FAIL-SAFE PERFORMANCE, SUCH AS LIFESUPPORT OR SAFETY DEVICES OR SYSTEMS, OR ANY OTHER APPLICATION THAT INVOKES THE POTENTIAL RISKS OF DEATH, PERSONAL INJURY OR PROPERTY OR ENVIRONMENTAL DAMAGE (“CRITICAL APPLICATIONS”). USE OF PRODUCTS IN CRITICAL APPLICATIONS IS AT THE SOLE RISK OF CUSTOMER, SUBJECT TO APPLICABLE LAWS AND REGULATIONS. ALL SPECIFICATIONS ARE SUBJECT TO CHANGE WITHOUT NOTICE.