

Overview

The purpose of this notification is to advise customers of a product marking change for Virtex®-6 FPGA packages. There is no change to the form, fit, or function.

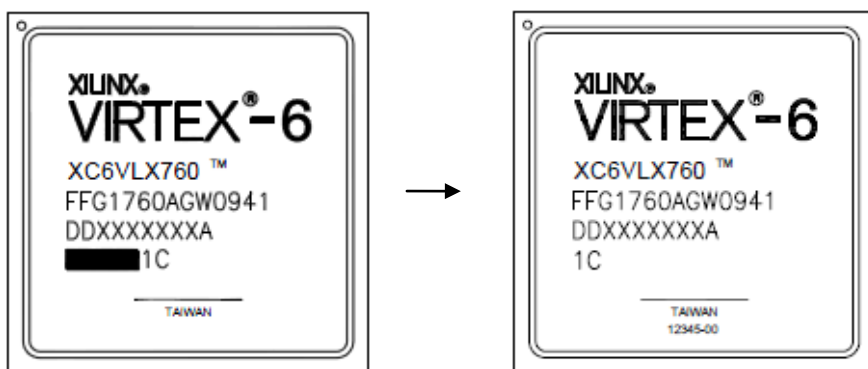
Description

Xilinx will be converting the package top mark from ink mark to laser mark and adding a device trace code for all Virtex-6 FPGA Flip Chip packages.

After the release of this notification, Xilinx will ship parts that are either ink mark or laser mark until all ink inventory is depleted.

Effective Date: April 25, 2011

Product marking example:



Current Marking (Black Ink)

New Marking (Laser)

Products Affected

This change affects all speed, package, and temperature variations of the commercial (C) and industrial (I) grade `XC` devices. Automotive `XA` and Military `XQ` devices are not affected by this notice. Affected part numbers are included in the [Table 1](#):

Table 1: Product Changes

Xilinx Product	Change Description
XC6VCX75T	Change from black ink marking to laser marking
XC6VCX130T	Change from black ink marking to laser marking
XC6VCX195T	Change from black ink marking to laser marking
XC6VCX240T	Change from black ink marking to laser marking
XC6VLX75T	Change from black ink marking to laser marking
XC6VLX130T	Change from black ink marking to laser marking
XC6VLX195T	Change from black ink marking to laser marking
XC6VLX240T	Change from black ink marking to laser marking
XC6VLX365T	Change from black ink marking to laser marking
XC6VLX550T	Change from black ink marking to laser marking
XC6VLX760	Change from black ink marking to laser marking
XC6VSX315T	Change from black ink marking to laser marking
XC6VSX475T	Change from black ink marking to laser marking
XC6VHX250T	Change from black ink marking to laser marking
XC6VHX255T	Change from black ink marking to laser marking
XC6VHX380T	Change from black ink marking to laser marking
XC6VHX565T	Change from black ink marking to laser marking

Response

No response is required. For additional information or questions, please contact Xilinx Technical Support <http://www.xilinx.com/support/techsup/tappinfo.htm>.

Important Notice: Xilinx Customer Notifications (XCNs, XDNs, and Quality Alerts) can be delivered via e-mail alerts sent by the Support website (<http://www.xilinx.com/support>). Register today and personalize your “Documentation and Design Advisory Alerts” area to include Customer Notifications. Xilinx Support provides many benefits, including the ability to receive alerts for new and updated information about specific products, as well as alerts for other publications such as data sheets, errata, application notes, etc. For information on how to sign up, refer to Answer Record 18683: <http://www.xilinx.com/support/answers/18683.htm>.

Revision History

The following table shows the revision history for this document.

Date	Version	Revision
04/25/11	1.0	Initial release.
05/16/11	1.1	Minor change to edit line 3 lot code from “1234567” to “DDXXXXXXXXA” on Product marking drawing example and added bottom line 2 trace code sample to New Marking (Laser) drawing.

Notice of Disclaimer

The information disclosed to you hereunder (the "Materials") is provided solely for the selection and use of Xilinx products. To the maximum extent permitted by applicable law: (1) Materials are made available "AS IS" and with all faults, Xilinx hereby DISCLAIMS ALL WARRANTIES AND CONDITIONS, EXPRESS, IMPLIED, OR STATUTORY, INCLUDING BUT NOT LIMITED TO WARRANTIES OF MERCHANTABILITY, NON-INFRINGEMENT, OR FITNESS FOR ANY PARTICULAR PURPOSE; and (2) Xilinx shall not be liable (whether in contract or tort, including negligence, or under any other theory of liability) for any loss or damage of any kind or nature related to, arising under, or in connection with, the Materials (including your use of the Materials), including for any direct, indirect, special, incidental, or consequential loss or damage (including loss of data, profits, goodwill, or any type of loss or damage suffered as a result of any action brought by a third party) even if such damage or loss was reasonably foreseeable or Xilinx had been advised of the possibility of the same. Xilinx assumes no obligation to correct any errors contained in the Materials, or to advise you of any corrections or update. You may not reproduce, modify, distribute, or publicly display the Materials without prior written consent. Certain products are subject to the terms and conditions of the Limited Warranties which can be viewed at <http://www.xilinx.com/warranty.htm>; IP cores may be subject to warranty and support terms contained in a license issued to you by Xilinx. Xilinx products are not designed or intended to be fail-safe or for use in any application requiring fail-safe performance; you assume sole risk and liability for use of Xilinx products in Critical Applications: <http://www.xilinx.com/warranty.htm#critapps>.