

ARM - Xilinx SoC Lab-in-a-Box One day workshop

Agenda

Time	
7:30 – 8:00 AM	Registration
8:00 – 8:30 AM	AUP Introduction XUP Introduction Introducing Lab in a Box
8:30 – 9:00 AM	Introduction to ARM [®] Cortex [®] -M0 processor & DesignStart [™]
9:00 – 9:30 AM	Overview of AMBA [®] 3 AHB-Lite Protocol
9:30 – 10:00 AM	Artix-7 FPGA Architecture
10:00 – 10:30 AM	Vivado Design Flow
10:30 – 11:30 PM	Lab 1: Simple AHB Peripheral Design and Integration
11:30 – 12:30 PM	Lunch
12:30 – 1:00 PM	Introduction to UART Peripheral
1:00 – 1:45 PM	Lab 2: Integrating UART Peripheral
1:45 – 2:15 PM	Introduction to Interrupts and CMSIS
2:15 – 3:30 PM	Lab 3: Integrating UART Peripheral and Interrupt. Use C program to develop interrupt handler
3:30 – 3:45 PM	Snake Game Application Demo
3:45 – 4:00 PM	Conclusion, Call for Action